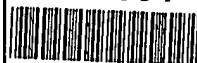


1c825 U.S. PTO
09/759639
01/16/01

257	395
Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

6528854



6528854

U.S. UTILITY Patent Application

<input checked="" type="checkbox"/> O.I.P.E. 094	PATENT DATE MAR 04 2003
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APPLICATION NO. 09/759639	CONT/PRIOR F	CLASS 257	SUBCLASS 395	ART UNIT 2818	EXAMINER T. Ho
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APPLICANTS
Masahiro Yoshida
Shunichi Tokitoh

TITLE
Semiconductor device and method for manufacturing the same

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
257	395	438	225	384				
INTERNATIONAL CLASSIFICATION								
H01L	29/76							
H01L	29/94							
H01L	31/119							

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. 23	Figs. Drwg. 37	Print Fig. 1	Total Claims 14	Print Claim for O.G. 1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<u>TU-TU + 10</u> 09/02 (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED 9-25-02	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	David Neims Supervisory Patent Examiner Technology Center 2800 8/02 (Primary Examiner) (Date)			ISSUE FEE Amount Due Date Paid \$1280.00 12-19-02	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	10/2/02 (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	

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